



UNIVERSITÀ
DEGLI STUDI
DI PALERMO

DEIM UNIPA – VIALE DELLE SCIENZE (EDIF. 9)

1° Short Course on VLSI Design

Non-Volatile-Memories: From Physic Principles to VLSI Design (an Industrial Perspective)

Teaching expert:

Ing. Francesco La Rosa

Senior Member of Technical Staff

NVM Design Manager within MMS Group R&D

STMicroelectronics – Rousset (FR) Catania and Palermo (IT)

When it will happen :

15:30 – 17:30 of the following days:

Week	Meeting
WK1	Tuesday 3 March
	Wednesday 4 March
WK2	Tuesday 17 March
	Wednesday 18 March
WK3	Tuesday 31 March
	Wednesday 1 April
WK4	Monday 20 April
	Tuesday 21 April
WK5	Monday 4 May
	Tuesday 5 May
WK6	Monday 18 May
	Tuesday 19 May

12 Meetings for a total of 24 teaching hours

Main Topics:

- Floating Gate NVM: Physics Main Concepts and related Architecture
- Flash NOR memory cell
- Flash NOR memory device internal Architecture
- VLSI Design of low & high voltage analog sub-systems for a Flash NOR memory device

Coordinator: **Costantino Giaconia**

Responsible for

Electronic Engineering Degrees



DEIM
Dipartimento di Energia,
ingegneria dell'Informazione
e modelli **Matematici**