



UNIVERSITÀ
DEGLI STUDI
DI PALERMO

DEIM UNIPA – VIALE DELLE SCIENZE (EDIF. 9)

2° Short Course on VLSI Design

Non-Volatile-Memories:
From Physic Principles
to VLSI Design
(an Industrial Perspective)

Teaching expert:

Ing. Francesco La Rosa

Senior Member of Technical Staff

NVM Design Manager within MDG Group R&D

STMicroelectronics – Rousset (FR) Catania and Palermo (IT)

When it will happen :

15:30 – 18:30 of the following days:

Week	Meeting
WK1	Tuesday, March 22 th
	Wednesday, March 23 th
WK2	Tuesday, April 12 th
	Wednesday, April 13 th
WK3	Tuesday, May 3 rd
	Wednesday, May 4 th
WK4	Tuesday, May 17 th
	Wednesday, May 18 th

Main Topics:

- Floating Gate NVM: Physics Main Concepts and related Architecture
- Flash NOR memory cell
- Flash NOR memory device internal Architecture
- VLSI Design of low & high voltage analog sub-systems for a Flash NOR memory device



8 Meetings for a total of

24 teaching hours

Free registration at

www.unipa.it/ccseln

Coordinator: **Costantino Giaconia**

Responsible for

Electronic Engineering Degrees



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